

Electrical Clock Recovery Modules

80A05/80A07 Modules for DSA8200*1 Series Oscilloscopes Data Sheet



Features & Benefits

- Electrical Clock Recovery for:
 - Enumerated Bit Rates between 50 Mb/s and 12.6 Gb/s
 - Continuously Variable Rates from 100 Mb/s to 12.5 Gb/s Enables Support for Current, Emerging, and Future Bit Rate Needs
- High Jitter Tolerance, Slow Slew Rate Tolerance for Recovering Clocks from Stressed, Degraded, or Spread-spectrum Clocked Signals
- Excellent Time Base Stability and Ultra-low Residual Jitter (as low as 250 f_{RMS}) for Best Measurement System Fidelity
- Unequaled PLL Bandwidth and Roll-off Shape Control (Tunable loop bandwidth of 100 kHz to 12 MHz) Enables Control Required by Many Compliance Standard Tests
- Clean 50 Ω Path for the Best Signal Acquisition Fidelity

Applications

- High-speed Serial Data Link and Device Characterization for Computer, Communications, and Consumer Applications
- Compliance Testing of Electrical Signaling
- High-speed Optical Communications Testing
- Jitter, Noise, BER, and Signal Impairment Analysis
- Testing of Frequency Agile or Multirate Devices

The **80A05 and 80A07 Electrical Clock Recovery Modules** enable clock recovery for electrical signals. Additionally, the 80A05 provides internal routing of the recovered clock for triggering of the 8200*1 Series oscilloscopes.

The **80A05** recovers clocks from serial data streams for all of the most common electrical standards in the 50 Mb/s to 3.188 Gb/s range (continuous coverage) plus the (fixed) rate of 4.25 Gb/s. Option 10G adds support for user-selectable rates in the following ranges:

- 3.267 to 4.250 Gb/s;
- 4.900 to 6.375 Gb/s; and
- 9.800 to 12.60 Gb/s range

The 80A05 provides high level of integration and the best sensitivity available, and is the optimal solution for testing of optical transmitter components and electrical components of optical systems. The 80A05 PLL loop bandwidth selection is automatic with no interaction, leading to easy setup and operation.

The 80A07 recovers clocks from serial data streams for all of the most common electrical standards in the continuous 100 Mb/s to 12.5 Gb/s range. Auto-locking capability is selectable from the user interface or programmatic interface, so the design and test engineers can search and lock onto signals of unknown data rates. The 80A07 offers complete configurability and state-of-the-art specifications and is the preferred solution for most serial data standards due to excellent stability, superior Jitter and slew rate tolerance for recovering clocks from stressed or degraded signals, and unequaled PLL bandwidth and roll-off shape control for either Golden PLL compliance testing or custom PLL response. The 80A07 also locks on spread-spectrum signals.

In addition to standard rate support, users can specify custom bit rates on either of the two modules in order to test devices, modules, and systems running at emerging or nonstandard rates. The wide clock recovery ranges support for user-specified bit rates provide complete clock recovery solutions for testing a wide range of computer, communications, and consumer electronics signaling rates and standards.

The 80C12 optical module with either the 80A05 or 80A07 clock recovery modules provides a complete solution for optical rates between 155 Mb/s and 12.6 Gb/s.

*1 Also compatible with CSA/TDS8200, CSA/TDS8000B, and CSA/TDS8000 sampling oscilloscopes.

Both modules accept either single-ended or differential signals at their inputs. With either single-ended or differential signals, the attenuated but otherwise unmodified input signal is available on output connectors on the front panel of the modules. The signal path to these front-panel outputs has been carefully designed to preserve signal fidelity well beyond the frequency corresponding to the maximum bit rate addressed by the clock recovery circuit. The front-panel output signals can therefore be connected to a high-frequency sampling module (such as 80E07) and be acquired for analysis while preserving high-frequency features of the signal.

Tektronix clock recovery solutions combine simplicity of use with excellent flexibility; the full rate recovered clock or its sub-rate is available on the modules' front panel to clock or trigger other equipment.

Characteristics

Supported Specifications

Specification		80A05		80A07
		Standard	Option 10G	
OC3/STM1	155.52 Mb/s	■	■	◆*2
OC12/STM4	622.08 Mb/s	■	■	■
Fibre Channel	1.063 Gb/s	■	■	■
Gigabit Ethernet	1.25 Gb/s	■	■	■
SATA Gen I	1.50 Gb/s	◆*3	◆*3	■
2 GB Fibre Channel	2.125 Gb/s	■	■	■
OC48/STM16	2.488 Gb/s	■	■	■
2 GB Ethernet	2.50 Gb/s	■	■	■
PCI Express I	2.50 Gb/s	◆*3	◆*3	■
Infiniband®	2.50 Gb/s	■	■	◆*2
2.5G G.709 FEC	2.666 Gb/s	■	■	◆*2
SATA Gen II	3.0 Gb/s	◆*3	◆*3	■
XAUI, 10GBase-X	3.125 Gb/s	■	■	◆*2
10 GB FibreChannel x4	3.188 Gb/s	■	■	◆*2
4 GB FibreChannel	4.25 Gb/s	■	■	■
FB-DIMM1	3.2, 4.0, 4.8 Gb/s		◆*2, 3	■
PCI Express II	5.0 Gb/s		◆*2, 3	■
FB-DIMM2	4.8, 6.4, 8.0, 9.6 Gb/s		◆*2, 3	■
OIF CEI	6+ Gb/s		◆*2	■
2x XAUI	6.25 Gb/s		■	◆*2
8 GB Fibre Channel	8.50 Gb/s			■
OC192/STM64	9.953 Gb/s		■	■
XFP/XFI	9.95-11.2		◆*2	■
10 G Base-W	9.953 Gb/s		■	
10 G Base-R	10.31 Gb/s		■	■
10 GB Fibre Channel	10.51 Gb/s		■	◆*2
G.975 FEC	10.66 Gb/s		■	◆*2
G.709 FEC	10.71 Gb/s		■	◆*2
OIF CEI	11+ Gb/s			■
10 GbE w/ FEC	11.10 Gb/s		■	◆*2
Super FEC	12.50 Gb/s		■	◆*2

Additional enumerated standard rates are supported with 8200*1 Series firmware releases higher than 2.4.x

Rates Supported: ■ = Filter, ◆ = Optical Clock Recovery

*2 The standard is not enumerated, but is supported as a custom rate.

*3 No spread-spectrum clocking support.

**Clock Recovery Ranges for custom (user specified) rates
(in addition to previous enumerated lists)**

80A05		80A07
Standard	Option 10G	
50 Mb/s to 3.188 Gb/s 4.25 Gb/s	50 Mb/s to 3.188 Gb/s 3.267 to 4.25 Gb/s 4.900 to 6.375 Gb/s 9.800 to 12.60 Gb/s	100 Mb/s to 12.5 Gb/s continuous

**Sensitivity: (clock recovery will lock, differential data is given
for each input)**

80A05		80A07
Standard	Option 10G	
Lowest Supported Rate to 2.70 Gb/s	Differential ≤ 8 mV _{p-p} Single ended 10 mV _{p-p}	Differential 15 mV (typical) Single ended 30 mV (typical)
2.70 to 11.19 Gb/s	Differential ≤ 12 mV _{p-p} Single ended 15 mV _{p-p}	
11.19 to 12.60 Gb/s	Differential ≤ 15 mV _{p-p} Single ended 20 mV _{p-p}	

**Sensitivity: (clock recovery will lock, differential data is given
for each input) (Continued)**

Characteristic	80A05		80A07
	Standard	Option 10G	
I/O			
I/O Connectors	SMA		APC 3.5
I/O Impedance	50 Ω		50 Ω

Input
+Data In and -Data In; complementary signals

Input Voltage:		
Absolute Maximum Nondestructive	2.5 V _{p-p} (each input)	± 5 V maximum
Absolute Maximum Operational	2.0 V _{p-p} (either input) 1.0 V _{p-p} (differential, each input)	
Maximum Input Signal Skew (+Data In to -Data In under which the unit will still meet its sensitivity specification)	50 Mb/s to 2.70 Gb/s: 20% of unit interval 2.70 to 12.6 Gb/s: 20 ps	100 Mb/s to 2.70 Gb/s: 20% of unit interval 2.70 to 12.6 Gb/s: 20 ps
Measured Edge Density	N/A	$\pm 1\%$
Measured Phase Deviation	N/A	Displayed as % _{RMS} and % _{p-p} , 10-90% _{p-p} available range
Coupling		
Recovered Clock Output (to Trigger Clock output)	AC	AC
Data Input to Data Out	DC	AC

Output
+Data Out and -Data Out; complementary signals

Attenuation +Data In to +Data Out -Data In to -Data Out	6.6 dB ± 0.6 dB (ratio of +Data Out / +Data In)	2 dB (min) 2.6 dB (typical) 3 dB (max)
Bandwidth (-3 dB) +Data In to +Data Out -Data In to -Data Out	≥ 20 GHz	≥ 20 GHz

Recovered TRIGGER CLOCK (80A05) / CLOCK (80A07) Output

Characteristic	80A05		80A07
	Standard	Option 10G	
Output Frequency			
Input Bit Rate <2.70 Gb/s	Input bit rate		Full rate divided by 1, 2, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, 24, 25, 28, 30, 32, 35, 36, 40, 42, 45, 48, 49, 50, 54, 56, 60, 63, 64, 70, 72, 80, 81, 90, 100, 108, 112, 120, 126, 128, 140, 144, 160, 162, 168, 180, 192, 196, 200, 216, 224, 240, 252, 256, 280, 288, 320, 324, 336, 360, 384, 392, 432, 448, 504, 512, 576, 648. Odd division ratios have non-50% duty cycles.
Input Bit Rate ≥2.70 Gb/s	Input bit rate / 16		
Loop Bandwidth	BW of (bit rate / 1666) till 3 Gb/s 4 MHz nominal at other ranges and BW <4 MHz at 9.953 and 10.3125 Gb/s		100 kHz – 12 MHz variable (see CRU Loop Bandwidth graph on next page for calibrated loop bandwidth region)
Loop Bandwidth Accuracy	Nominal		±5%
Locking Range	1000 ppm nominal		50 MHz, adjustable to 10-500 MHz
Peaking	N/A		0-6 dB from 500 kHz to 12 MHz 0 dB from 100 to 500 kHz
Peaking Accuracy	N/A		Greater of ±10% of settings, or 0.2 dB
Jitter			
155 Mb/s to 2.70 Gb/s	0.5% of unit interval (RMS) typical		70 fs _{RMS} (typical), 250 fs _{RMS} (max) (measured at 800 mV _{p-p} input amplitude, 10 Gb/s, 1010 pattern, 2 MHz loop bandwidth setting and 0.5 dB peaking)
2.70 to 6.38 Gb/s	1.27 ps _{RMS} typical		
9.80 to 12.6 Gb/s	0.6ps _{RMS} typical		
Return loss	DC to 10 GHz: 15 dB 10 GHz to 20 GHz: >10 dB		15 dB
Rise/Fall Times	TRIGGER CLOCK output: <300 ps typical (10-90%)	10 G CLOCK output: <30 ps typical (10-90%)	25 ps (typical), 30 ps (max) (20/80%)
Amplitude	>400 mV (typical)		250 mV (min), 1.5 V (max), 900 mV _{p-p} (typical), accuracy greater of 10% or 30 mV
Output Frequency Deviation Tracking Range (Tracking 30 to 33 kHz Triangle Modulated SSC)	N/A		+500/-5500 ppm (+0.05/-0.55%)

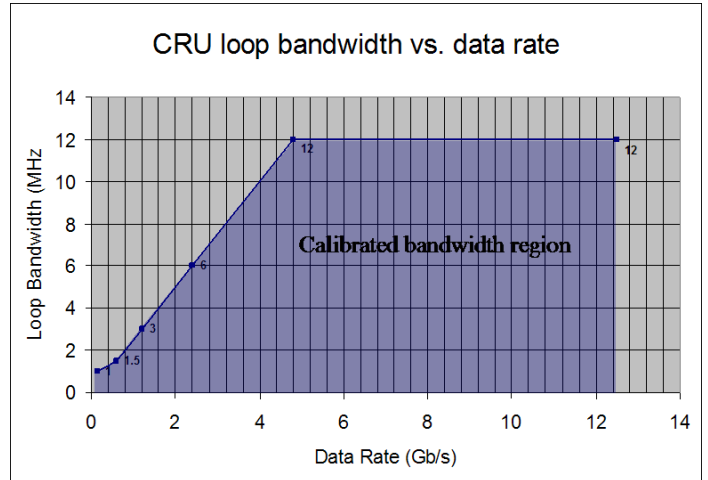
Recovered 10G CLOCK OUT (Option 10G only)

Characteristic	80A05		80A07
	Standard	Option 10G	
Jitter			
2.70 to 3.14 Gb/s	N/A	4x input bit rate; 2.5 pSRMS	N/A
3.27 to 4.25 Gb/s		3x input bit rate; 2.5 pSRMS	
4.90 to 6.38 Gb/s		2x input bit rate; 2.5 pSRMS	
9.80 to 12.6 Gb/s		1x input bit rate; <2.0 pSRMS	
Amplitude		>500 mV (typical)	
Trigger Output (80A07 Rear panel) (External lock status indication)			
Interface Type	N/A	SMA, 50 Ω DC coupled	
Latency	N/A	300 ms	
Trigger Input (80A07 Rear panel) (External clock recovery lock request)			
Interface type	N/A	SMA, 50 Ω DC coupled to 0 V	
Threshold	N/A	1.5 V	
Minimum Pulse Width	N/A	50 ns	

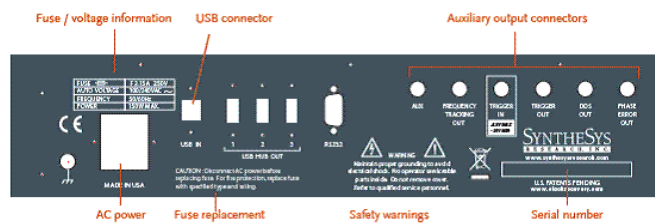
Physical Characteristics

Dimension	80A05	80A07
Width	6.5 in. / 165 mm	15.5 in. / 394 mm
Height	1.0 in. / 25 mm	3.75 in. / 95 mm
Depth	12.0 in. / 305 mm	13.75 in. / 337 mm
Weight	2.7 lb. / 1.22 kg	20 lb. / 9.1 kg

80A07 Clock Recovery Loop Bandwidth vs. Data Rate



The 80A07 clock recovery module has a variable loop bandwidth from 100 kHz to 12 MHz. The calibrated loop bandwidth is limited as shown on the graph above.



Rear Panels

Ordering Information

80A05

Multirate Electrical Clock Recovery Module

Includes: User Manual.

Note: Also used for 80C12 Optical Sampling Module Clock Recovery.

Product Options

- Opt. 10G** – Add bit rates:
3.267 Gb/s to 4.25 Gb/s.
4.900 Gb/s to 6.376 Gb/s.
9.800 Gb/s to 12.60 Gb/s.

Service Options

- Opt. C3** – Calibration Service 3 Years.
- Opt. C5** – Calibration Service 5 Years.
- Opt. D1** – Calibration Data Report.
- Opt. D3** – Calibration Data Report (with C3 only).
- Opt. D5** – Calibration Data Report (with C5 only).
- Opt. R3** – Extend repair warranty to 3 Years.
- Opt. R5** – Extend repair warranty to 5 Years.

Firmware support – This module is supported on 8200*1 Series oscilloscopes running Firmware release 2.0.1.5 or later.

80A07

Multirate clock recovery module.

Includes: User manual, Application CD (includes virtual front-panel application and remote control software developers kit), USB cable, US power cord, SMA terminations for data outputs, and quick start guide. A replacement fuse is also included.

Power Requirements

Line Voltage and Frequency – 100 to 240 V_{AC} 50/60 Hz.

Environmental Characteristics

- Temperature** –
Operating: 0 to 40 °C.
Nonoperating: -18 to 60 °C.
- Relative Humidity** –
Operating: 20 to 80% at or below 40 °C.
Nonoperating: 5 to 90% at or below 60 °C.
- Vibration (5 Hz to 500 Hz)** –
Operating: 0.2 g_{RMS}.
Nonoperating: 2.09 g_{RMS}.

Service Options

- C3** – Calibration Service 3 Years.
- C5** – Calibration Service 5 Years.
- R3** – Repair Service 3 Years.
- R5** – Repair Service 5 Years.
- CA1** – Provides a single calibration event or coverage for the designated calibration interval, whichever comes first.

International Power Plug Options

- Opt. A0** – North America power.
- Opt. A1** – Universal EURO power.
- Opt. A2** – United Kingdom power.
- Opt. A6** – Japan power.
- Opt. A10** – China power.

Interconnect Cables

015-0560-xx – (450 mm/18 in.; 1 dB loss at 20 GHz) cable is a high-quality cable recommended for work to 20 GHz.

Interconnect Cables (3rd party)

Tektronix recommends using quality high-performance interconnect cables with Tektronix high-bandwidth products in order to minimize measurement degradation and variations. The W.L. Gore & Associates' cable assemblies are compatible with the 2.92 mm, 2.4 mm, and 1.85 mm connector interface. Assemblies can be ordered by contacting Gore by phone at (800) 356-4622, or on the web at www.gore.com/tektronix.

Firmware Support

The 80A07 is a standalone clock recovery module and as such has its own front-panel controls. In addition, it ships with a virtual front-panel control application which can be installed on 8200*1 Series sampling oscilloscopes running version 2.0 or later or on a Windows PC running Windows 2000 or Windows XP. The controlling processor (8200*1 Series oscilloscope or Windows PC) must be connected to the 80A07 by a USB cable in order for this virtual front-panel control application to work. When running on an 8200*1 Series sampling oscilloscope or Windows PC, the virtual front-panel control application can be started from the Windows desktop or start menu. Additionally, if the 8200*1 Series sampling oscilloscope is running firmware version 2.4 or later, this control application can be invoked using the **Apps** menu within the sampling oscilloscope application. Remote control of the 80A07 is available with a software development kit which comes standard with the 80A07.

*1 Also compatible with CSA/TDS8200, CSA/TDS8000B, and CSA/TDS8000 sampling oscilloscopes.



Product(s) are manufactured in ISO registered facilities.



Product(s) complies with IEEE Standard 488.1-1987, RS-232-C, and with Tektronix Standard Codes and Formats.

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